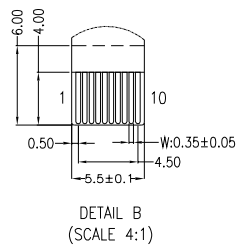
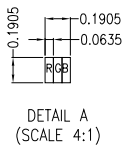
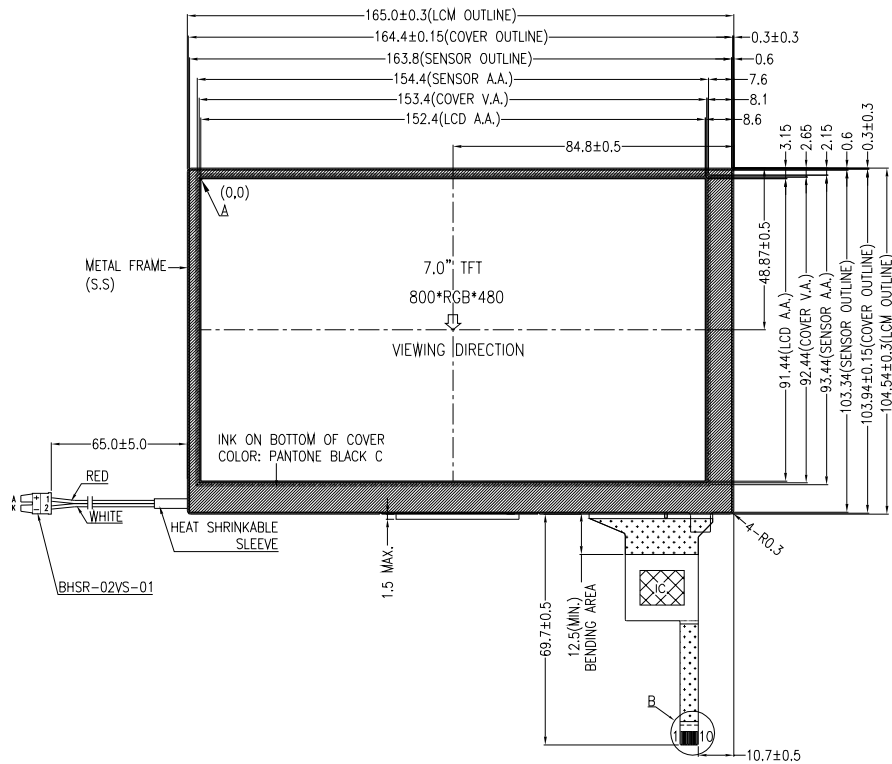


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



NOTES FOR TFT:

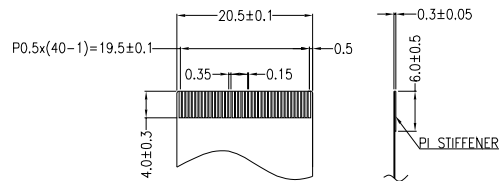
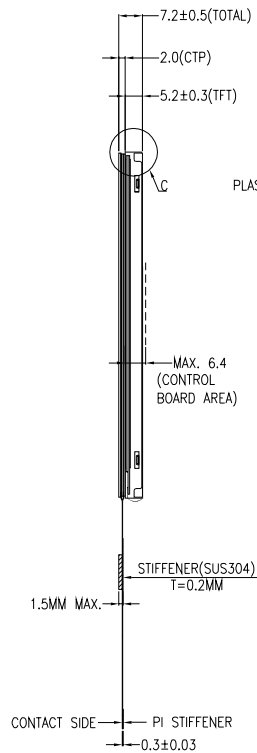
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: 12:00 O'CLOCK
4. DRIVER IC: EK79713B + EK73202A
5. BACKLIGHT: 24-CHIP WHITE LED, If=160mA, Vf=8.4V~10.2V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

NOTES FOR OTHERS:

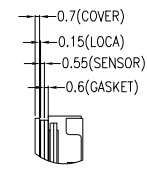
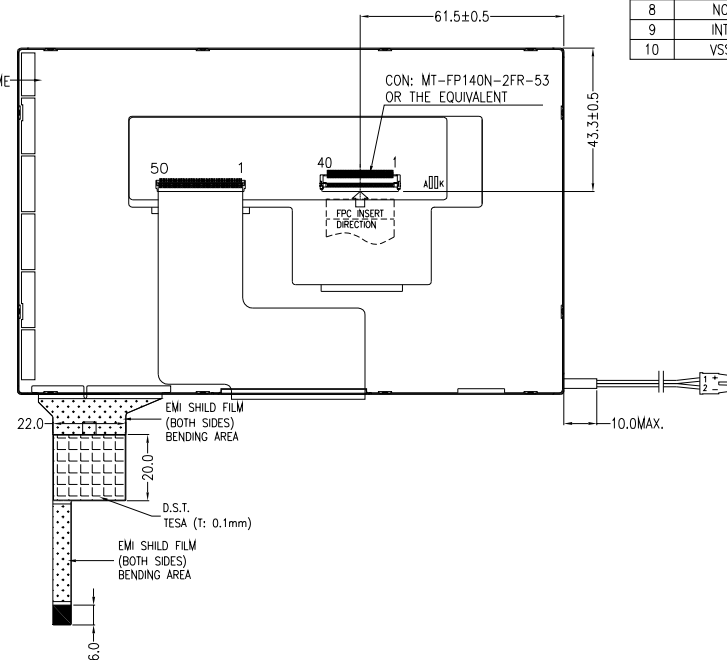
1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 425CD/M² (TYP.) WITH CTP
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

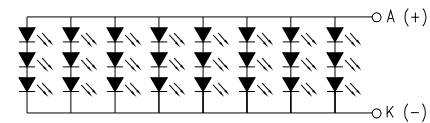
1. 7.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCC
3. OPERATING VOLTAGE: VDD=3.3V
4. DRIVER IC: FT7311DQQ
5. INTERFACE: IIC
6. RESOLUTION: 800*480
7. MULTI FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: 7H
9. FIRMWARE FILE: TBD



CONNECTOR MATED FPC/FFC STYLE
(SCALE 2:1)



DETAIL C
(SCALE 2:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN DESCRIPTION FOR CTP		PIN DESCRIPTION FOR LCM	
PIN	SYMBOL	PIN	SYMBOL
1	VSS	1	GND
2	VDD	2	GND
3	SCL	3	NC
4	NC	4	VCC
5	SDA	5	VCC
6	NC	6	VCC
7	RST	7	VCC
8	NC	8	NC
9	INT	9	DE
10	VSS	10	GND
		11	GND
		12	GND
		13	B5
		14	B4
		15	B3
		16	GND
		17	B2
		18	B1
		19	B0
		20	GND
		21	G5
		22	G4
		23	G3
		24	GND
		25	G2
		26	G1
		27	G0
		28	GND
		29	R5
		30	R4
		31	R3
		32	GND
		33	R2
		34	R1
		35	R0
		36	GND
		37	GND
		38	DCLK
		39	GND
		40	GND



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2024.05.21		MODULE P/N:
CHECKED BY:			SCALE : 1/1	M0700A2T-38CPA
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT + CTP MODULE

01	FIRST ISSUE	RITA	2024.05.21
VER.	REVISED DESCRIPTION	REVISER	DATE